

**In the Claims**

Please amend the claims as follows:

1. (cancelled)
2. (previously presented) A method of interfacing frame based TDM telecommunications traffic in which each TDM frame supports a plurality of data structures each comprising one or more channels from a frame-based TDM network to an asynchronous network in which traffic is transported in cells or packets, the method comprising;
  - issuing credits at a substantially constant rate;
  - assigning the credits to each said data structure according to the size of that data structure;
  - determining for each said data structure a threshold number of assigned credits; and,
  - when said threshold value is reached, assembling that data structure into cells or packets for despatch into the asynchronous network.
3. (original) A method as claimed in claim 2, wherein said credits are assigned via a connection control.
4. (previously presented) A method as claimed in claim 3, wherein said connection control assigns credits to a data structure by writing the identity of that data structure into free locations in a reverse channel map.
5. (original) A method as claimed in claim 4, wherein said credit assignments are stored in a random access memory having memory locations one for each TDM channel.

6. (original) A method as claimed in claim 5, wherein said credits are issued in cycles corresponding to the TDM frame period.

7. (original) A method as claimed in claim 6, wherein the rate of issuance of credits is greater than the TDM channel rate.

8. (original) A method as claimed in claim 7, wherein said asynchronous network is an ATM network.

9. (cancelled)

10. (currently amended) An interface arrangement for interfacing frame based TDM telecommunications traffic in which each TDM frame supports a plurality of data structures each comprising one or more channels from a frame-based TDM network to an asynchronous network in which traffic is transported in cells or packets, the method interface arrangement comprising;

means for issuing credits at a substantially constant rate;

means for assigning the credits to each said data structures according to the size of that data structure; and

means for determining for each said data structure a threshold number of assigned credits whereby, when said threshold value is reached, the data structure is assembled into cells or packets for despatch into the asynchronous network.

11. (original) An interface arrangement as claimed in claim 10, wherein said credit assignment means comprises a connection control.

12. (original) An interface arrangement as claimed in claim 11, wherein said connection control assigns credits to a data structure by writing the identity of that data structure into free locations in a reverse channel map.

13. (original) An interface arrangement as claimed in claim 12, wherein said credit assignments are stored in a random access memory having memory locations one for each TDM channel.

14. (original) An interface arrangement as claimed in claim 13, and incorporating means for adjusting the rate at which credits are issued by the credit issuing means.

15. (original) An interface arrangement as claimed in claim 14, and incorporating a clock having a first clock rate corresponding to the TDM frame rate and whereby the credit issuing means issues said credits on a cyclic basis.

16. (original) An interface arrangement as claimed in claim 15, wherein said clock has a second clock rate corresponding to the channel rate of said TDM frames, said second clock rate providing a timing signal for said credit issuing means whereby said credits are issued at a rate at least equal to the channel rate.

17. (previously presented) A method of scheduling despatch of a TDM data structure packetized into cells or packets for despatch into an asynchronous network, the method comprising assigning credits to the data structure in accordance with the size of said data structure, comparing an accumulated total of credits assigned to said data structure, and, when said total reaches a predetermined threshold, despatching said cells into the asynchronous network at a substantially constant rate.